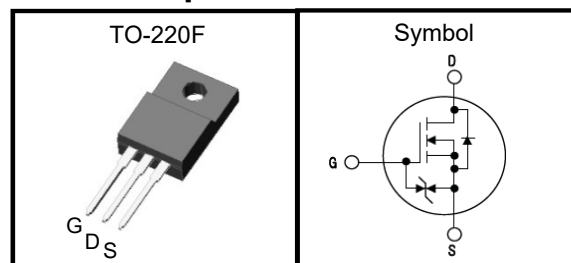


**650V N Channel Super Junction MOSFET**
**Feature**

- Very Low FOM ( $R_{DS(on)} \times Q_g$ )
- Extremely low switching loss
- Excellent stability and uniformity
- 100% Avalanche Tested
- Built-in ESD Diode

**Applications**

- Switch Mode Power Supply
- Uninterruptible Power Supply
- Power Factor Correction
- TV power

**Pin Description**


$V_{DSS}$	650	V
$R_{DS(ON)-Typ}$	0.32	$\Omega$
$I_D$	11	A

**Absolute Maximum Ratings**  $T_C=25^\circ\text{C}$  unless otherwise specified

Symbol	Parameter	Value	Unit
$V_{DSS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ )	11	A
	Drain Current - Continuous ( $T_C = 100^\circ\text{C}$ )	6.6	A
$I_{DM}^{(1)}$	Drain Current - Pulsed	30	A
$E_{AS}^{(2)}$	Single Pulsed Avalanche Energy	140	mJ
$I_{AR}$	Avalanche Current	1.8	A
dv/dt	MOSFET dv/dt ruggedness, $V_{DS}=0\dots 520\text{V}$	50	V/ns
dv/dt	Reverse diode dv/dt, $V_{DS}=0\dots 520\text{V}$ , $I_{DS}\leq I_D$	15	V/ns
$P_D$	Power Dissipation ( $T_C = 25^\circ\text{C}$ )	30	W
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

**Thermal Resistance Characteristics**

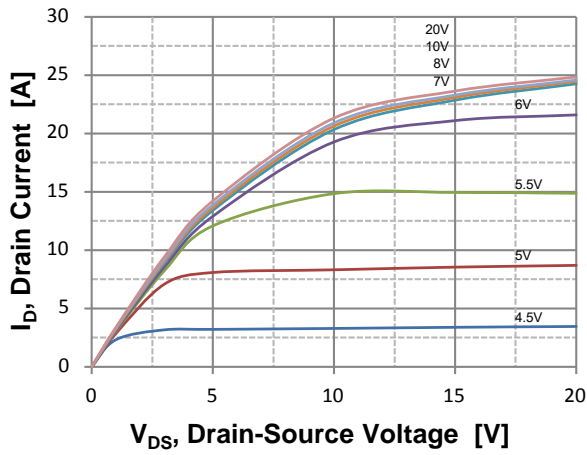
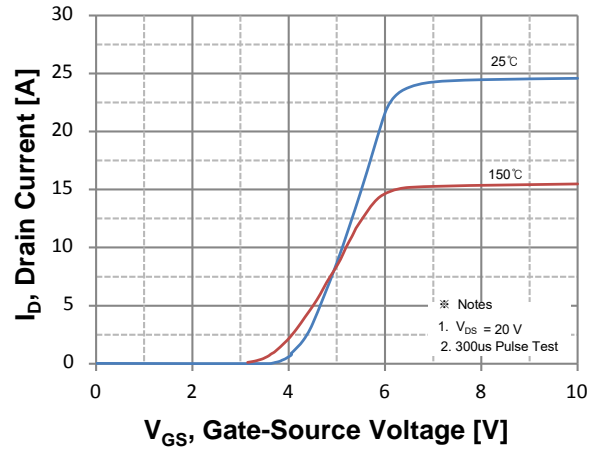
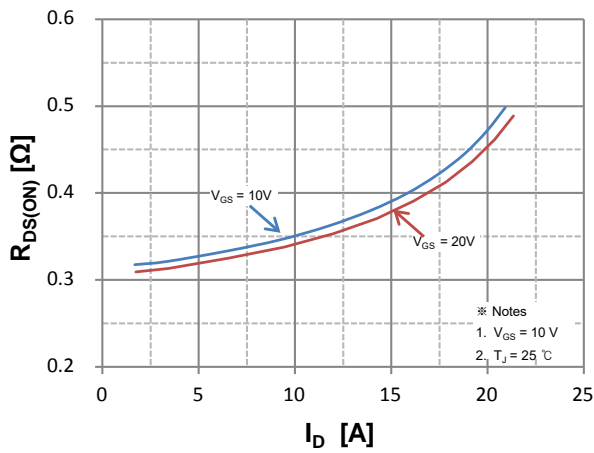
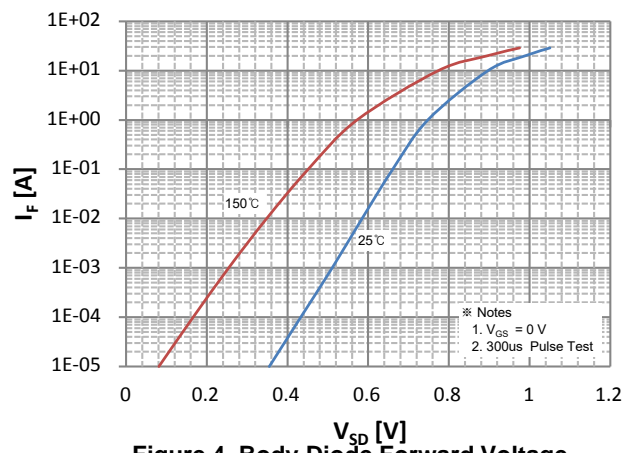
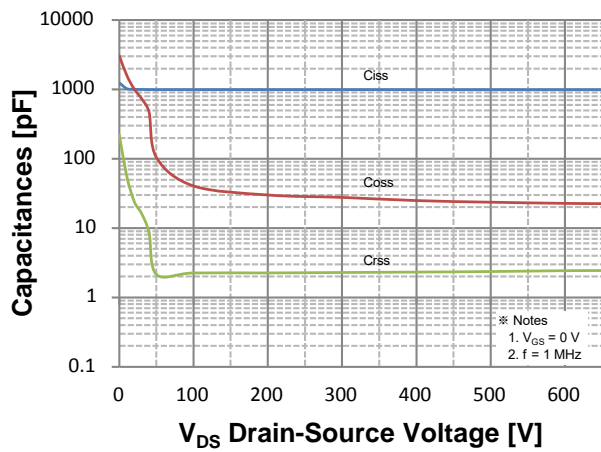
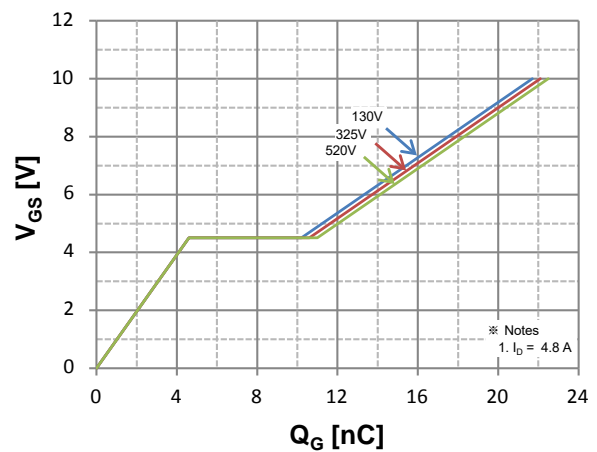
Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	4.0	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	$^\circ\text{C}/\text{W}$

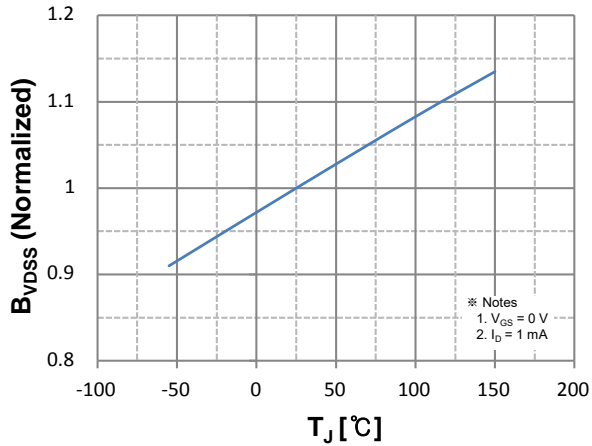
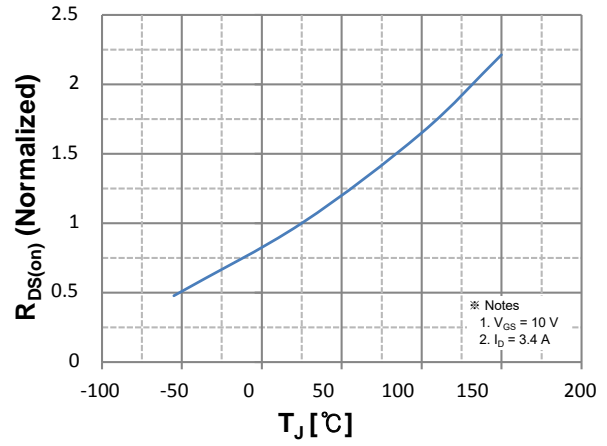
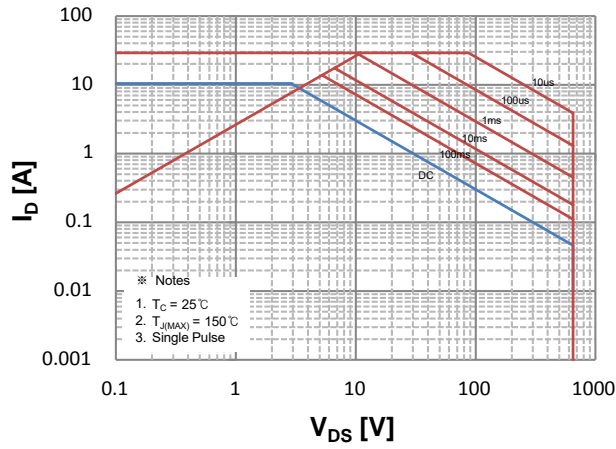
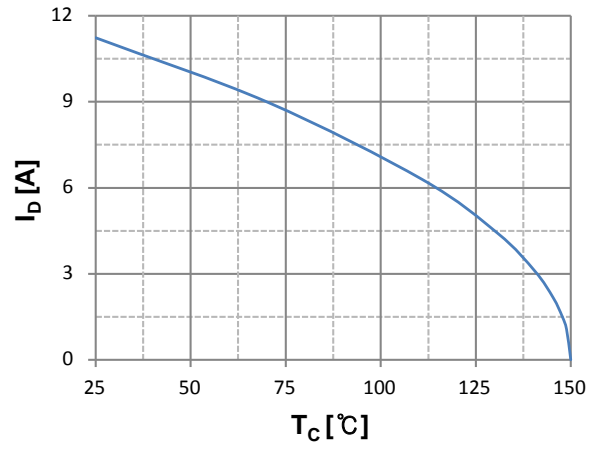
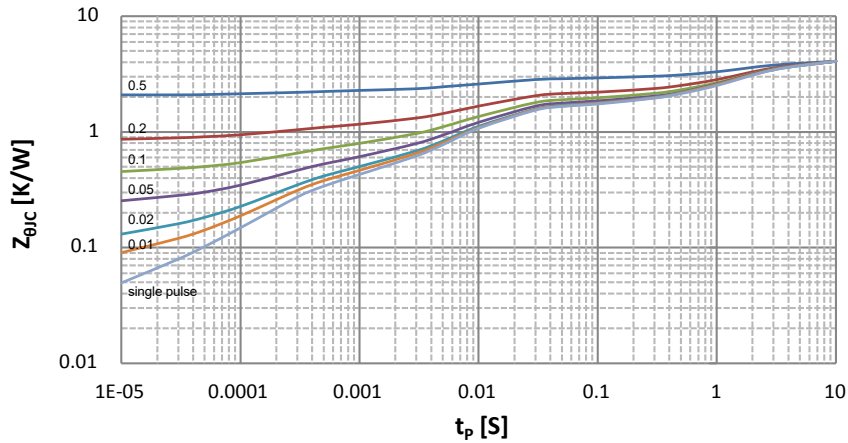

**650V N Channel Super Junction MOSFET**
**Electrical Characteristics**  $T_J=25\text{ }^\circ\text{C}$  unless otherwise specified

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>On Characteristics</b>						
$V_{GS}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 370\ \mu\text{A}$	2.0	-	4.0	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\ \text{V}, I_D = 3.4\ \text{A}$	-	0.32	0.38	$\Omega$
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\ \text{V}, I_D = 1\ \text{mA}$	650	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 650\ \text{V}, V_{GS} = 0$	-	-	1	$\mu\text{A}$
		$V_{DS} = 650\ \text{V}, T_C = 150\text{ }^\circ\text{C}$	-	-	100	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS} = \pm 20\ \text{V}, V_{DS} = 0\ \text{V}$	-	-	$\pm 1$	$\mu\text{A}$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 100\ \text{V}, V_{GS} = 0\ \text{V}, f = 1.0\ \text{MHz}$	-	980	-	pF
$C_{oss}$	Output Capacitance		-	40	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	2.3	-	pF
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Time	$V_{DS} = 325\ \text{V}, I_D = 4.8\ \text{A}, R_G = 25\ \Omega$ (Note 3,4)	-	30	-	ns
$t_r$	Turn-On Rise Time		-	23	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	190	-	ns
$t_f$	Turn-Off Fall Time		-	20	-	ns
$Q_g$	Total Gate Charge	$V_{DS} = 520\ \text{V}, I_D = 4.8\ \text{A}, V_{GS} = 10\ \text{V}$ (Note 3,4)	-	23	-	nC
$Q_{gs}$	Gate-Source Charge		-	4.7	-	nC
$Q_{gd}$	Gate-Drain Charge		-	6.5	-	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current		-	-	10.4	A
$I_{SM}$	Maximum Pulsed Drain-Source Diode Forward Current		-	-	30	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = 0\ \text{V}, I_S = 4.8\ \text{A}$	-	-	1.3	V
$t_{rr}$	Reverse Recovery Time	$V_{GS} = 0\ \text{V}, I_S = 4.8\ \text{A}, di_F/dt = 100\ \text{A}/\mu\text{s}$	-	240	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	2.3	-	$\mu\text{C}$

**Notes :**

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2.  $I_{AS}=1.8\ \text{A}, V_{DD}=50\ \text{V}, R_G=25\ \Omega$ , Starting  $T_J=25\text{ }^\circ\text{C}$
3. Pulse Test : Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2\%$

**650V N Channel Super Junction MOSFET**
**Typical Characteristics**

**Figure 1. On Region Characteristics**

**Figure 2. Transfer Characteristics**

**Figure 3. On Resistance Variation vs Drain Current and Gate Voltage**

**Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature**

**Figure 5. Capacitance Characteristics**

**Figure 6. Gate Charge Characteristics**

**650V N Channel Super Junction MOSFET**
**Typical Characteristics**

**Figure 7. Breakdown Voltage Variation vs. Temperature**

**Figure 8. On-Resistance Variation vs. Temperature**

**Figure 9. Maximum Safe Operating Area**

**Figure 10. Maximum Drain Current vs. Case Temperature**

**Figure 11. Transient Thermal Response Curve**

**650V N Channel Super Junction MOSFET**
**TO-220F Package Outline Data**

unit: mm

