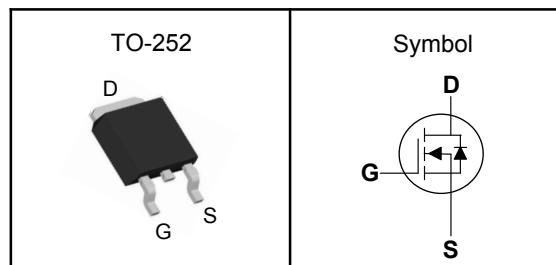


650V Super Junction Power MOSFET

Features

- Low drain-source on-resistance: $R_{DS(ON)}=0.23\Omega(\text{typ})$
- Easy to control gate switching
- Enhancement mode: $V_{th} = 2.0 \text{ to } 4.0\text{V}$
- 100% avalanche tested
- RoHS compliant

Pin Description



Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger, Lighting

V_{DSS}	650	V
$R_{DS(ON)-\text{Typ}}$	230	$\text{m}\Omega$
I_D	15	A

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
V_{DSS}	Drain-Source Voltage	650	V
V_{GSS}	Gate-Source Voltage	± 30	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
E_{AS}	Single Pulse Avalanche Energy ^③	290	mJ
$I_{DM}^{①}$	Pulse Drain Current Tested	45	A
I_D	Continuous Drain Current	15	A
P_D	Maximum Power Dissipation	100	W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}^{③}$	Thermal Resistance-Junction to Ambient	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ₁	1.6	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.



FS65R260D

650V Super Junction Power MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=250\mu\text{A}$	650	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=650\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$	---	---	1	μA
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}$, $\text{I}_D=250\mu\text{A}$	2	---	4	V
I_{GSS}	Gate Leakage Current	$\text{V}_{\text{GS}}=\pm 30\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
$\text{R}_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=7.5\text{A}$	---	230	260	$\text{m}\Omega$
Dynamic Characteristics^⑤						
C_{iss}	Input Capacitance	$\text{V}_{\text{GS}}=0\text{V}$, $\text{V}_{\text{DS}}=100\text{V}$, Freq.=1MHz	---	1170	---	pF
C_{oss}	Output Capacitance		---	51	---	
C_{rss}	Reverse Transfer Capacitance		---	7	---	
$\text{T}_{\text{d(on)}}$	Turn-on Delay Time	$\text{V}_{\text{DD}}=400\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{R}_G=25\Omega$, $\text{I}_D=15\text{A}$	---	25	---	nS
T_r	Turn-on Rise Time		---	65	---	
$\text{T}_{\text{d(off)}}$	Turn-off Delay Time		---	105	---	
T_f	Turn-off Fall Time		---	50	---	
R_g	Gate Resistance	$f = 1.0\text{MHz}$, open drain	---	12	---	Ω
Q_g	Total Gate Charge	$\text{V}_{\text{DS}}=400\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=15\text{A}$	---	27	---	nC
Q_{gs}	Gate-Source Charge		---	5.5	---	
Q_{gd}	Gate-Drain Charge		---	10.5	---	
Source-Drain Characteristics ($T_J=25^\circ\text{C}$)						
$\text{V}_{\text{SD}}^{④}$	Diode Forward Voltage	$\text{I}_S=15\text{A}$, $\text{V}_{\text{GS}}=0\text{V}$	---	0.9	1.2	V
t_{rr}	Reverse Recovery Time	$\text{V}_R=400\text{V}$, $\text{I}_F=15\text{A}$, $d\text{i}/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	410	---	nS
Q_{rr}	Reverse Recovery Charge		---	4	---	nC

Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

650V Super Junction Power MOSFET

Typical Characteristics

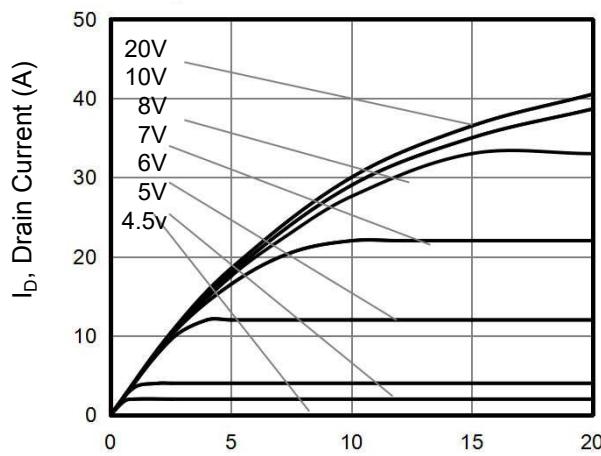


Figure 1. Output Characteristics

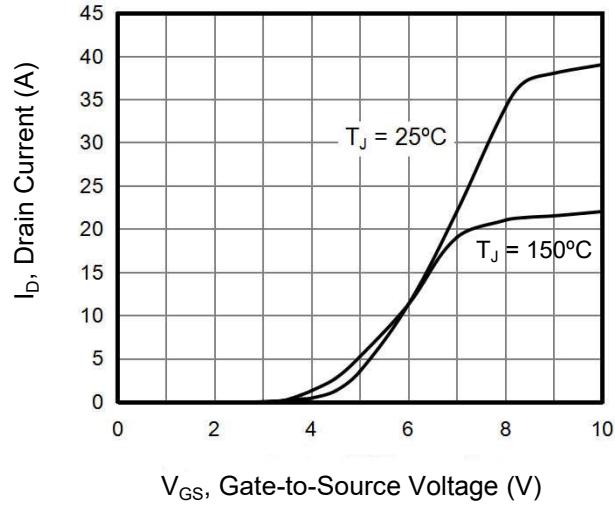


Figure 2. Transfer Characteristics

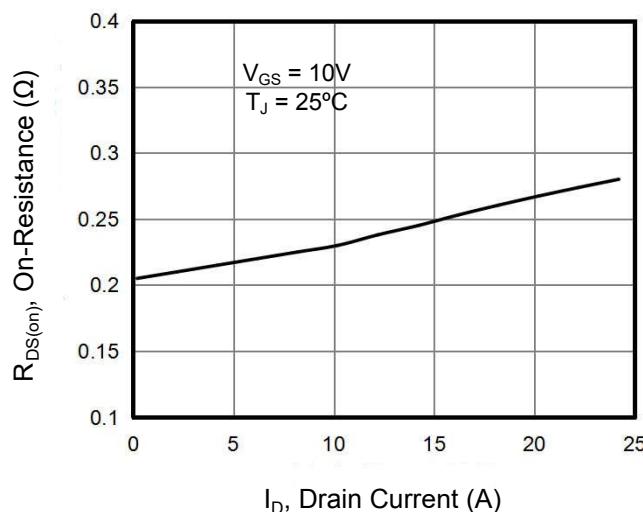


Figure 3. On-Resistance vs. Drain Current

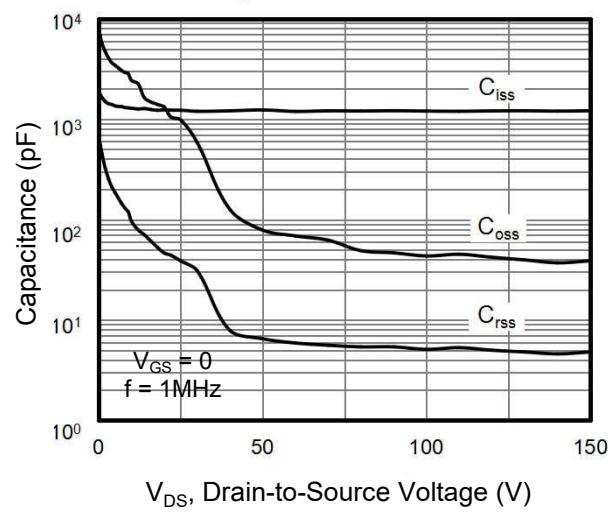


Figure 4. Capacitance

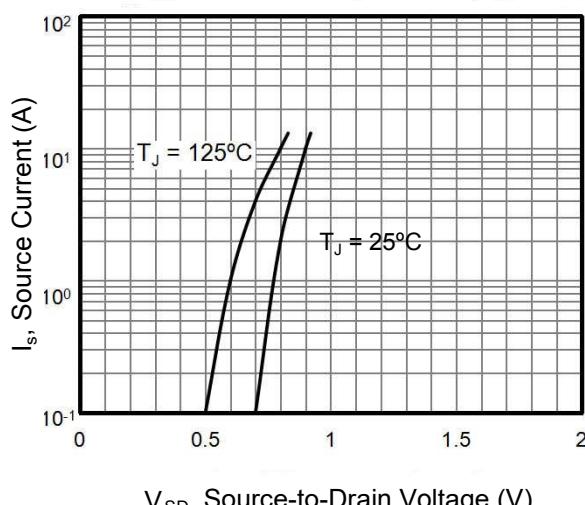
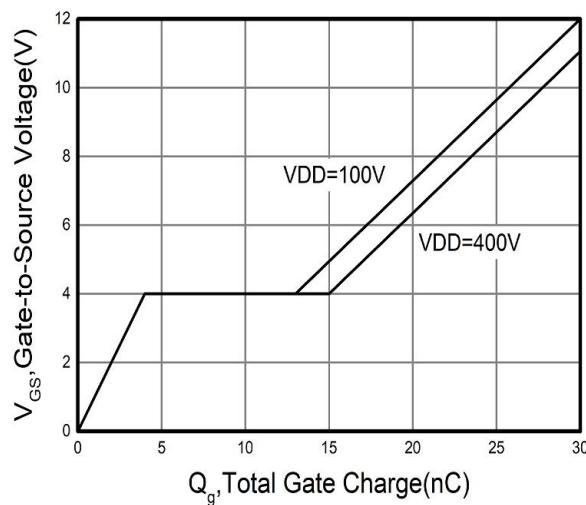
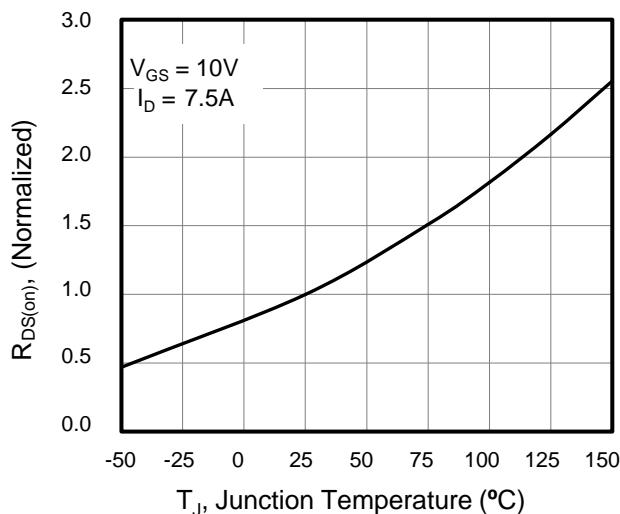
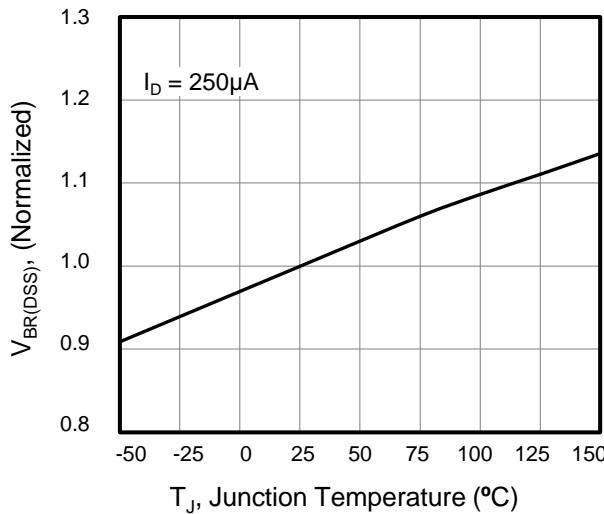


Figure 6. Body Diode Forward Voltage

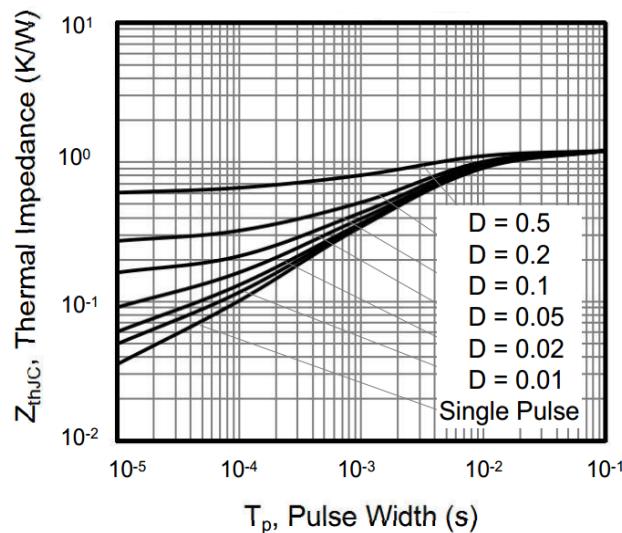
650V Super Junction Power MOSFET



**Figure 7. On-Resistance
vs. Junction Temperature**



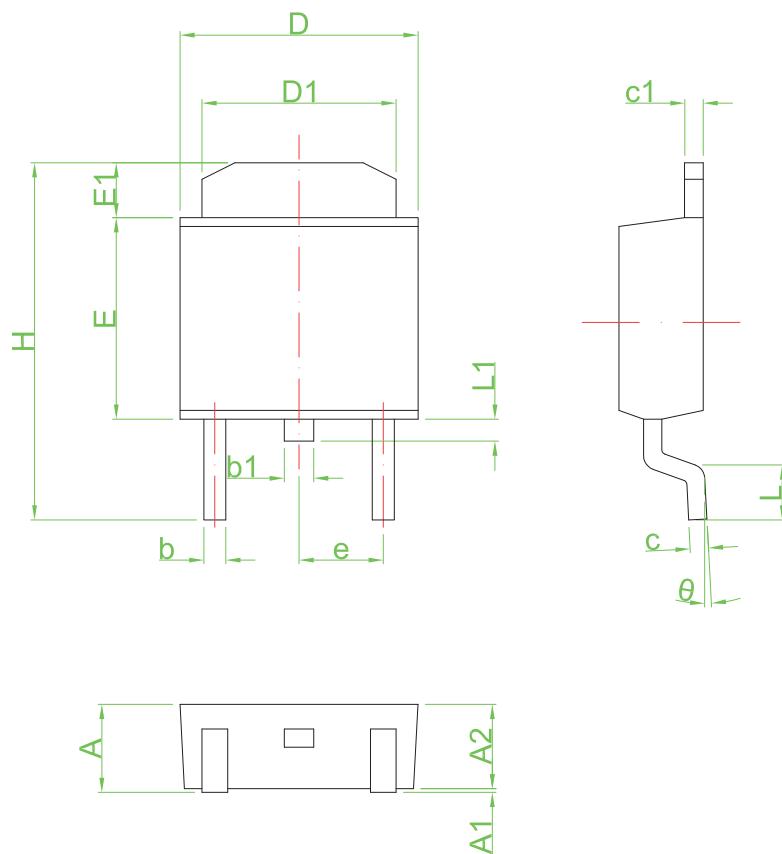
**Figure 8. Breakdown voltage
vs. Junction Temperature**



**Figure 9. Transient Thermal Impedance
TO-252**

650V Super Junction Power MOSFET

TO-252 Package Outline Dimensions



Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	2.25	2.65	0.089	0.104
A1	0.00	0.15	0.000	0.006
A2	2.20	2.40	0.087	0.094
b	0.50	0.70	0.020	0.028
b1	0.70	0.90	0.028	0.035
c	0.46	0.66	0.018	0.026
c1	0.46	0.66	0.018	0.026
D	6.30	6.70	0.248	0.264
D1	5.20	5.40	0.205	0.213
E	5.30	5.70	0.209	0.224
E1	1.40	1.60	0.055	0.063
H	9.40	9.90	0.370	0.390
e	2.30 TYP		0.09 TYP	
L	1.40	1.77	0.055	0.070
L1	0.50	0.70	0.020	0.028
θ	0°	8°	0°	8°