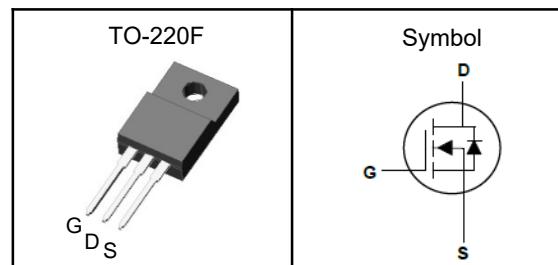


600V Super Junction Power MOSFET

Features

- Low drain-source on-resistance: $R_{DS(ON)}=0.14\Omega(\text{typ})$
- Easy to control gate switching
- Enhancement mode: $V_{th} = 2.5$ to $3.5V$
- 100% avalanche tested
- RoHS compliant

Pin Description



Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger, Lighting

V_{DSS}	600	V
$R_{DS(ON)-\text{Typ}}$	140	$\text{m}\Omega$
I_D	20	A

Absolute Maximum Ratings ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
V_{DSS}	Drain-Source Voltage	600	V
V_{GSS}	Gate-Source Voltage	± 30	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
E_{AS}	Single Pulse Avalanche Energy ^③	480	mJ
$I_{DM}^{①}$	Pulse Drain Current Tested	60	A
I_D	Continuous Drain Current	$T_c=25^\circ\text{C}$	20
P_D	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	34
			W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	80	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ₁	4	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.



FS60R160F

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Electrical Characteristics ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	600	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=200\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	μA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	2.5	---	3.5	V
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=10\text{A}$	---	140	160	$\text{m}\Omega$
Dynamic Characteristics^⑤						
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=100\text{V}$, Freq.=1MHz	---	1724	---	pF
C_{oss}	Output Capacitance		---	72	---	
C_{rss}	Reverse Transfer Capacitance		---	6	---	
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=400\text{V}$, $I_D=20\text{A}$, $V_{\text{GS}}=10\text{V}$, $R_G=25\Omega$	---	25	---	nS
T_r	Turn-on Rise Time		---	59	---	
$T_{\text{d(off)}}$	Turn-off Delay Time		---	121	---	
T_f	Turn-off Fall Time		---	44	---	
Q_g	Total Gate Charge	$V_{\text{DS}}=400\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	---	38.5	---	nC
Q_{gs}	Gate-Source Charge		---	8	---	
Q_{gd}	Gate-Drain Charge		---	15	---	
Source-Drain Characteristics ($T_J=25^\circ\text{C}$)						
V_{SD}	Diode Forward Voltage ₂	$V_{\text{GS}}=0\text{V}$, $I_S=20\text{A}$, $T_J=25^\circ\text{C}$	---	0.9	1.2	V
t_{rr}	Reverse Recovery Time	$V_R=400\text{V}$, $I_F=20\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	453	---	nS
Q_{rr}	Reverse Recovery Charge		---	5.1	---	nC

Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

600V Super Junction Power MOSFET

Typical Characteristics

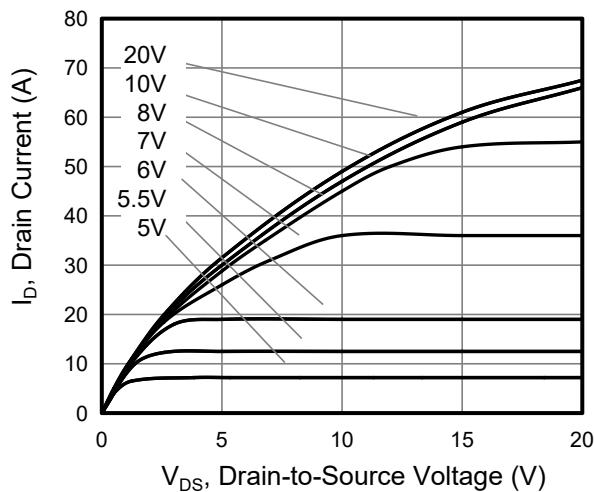


Figure 1. Output Characteristics

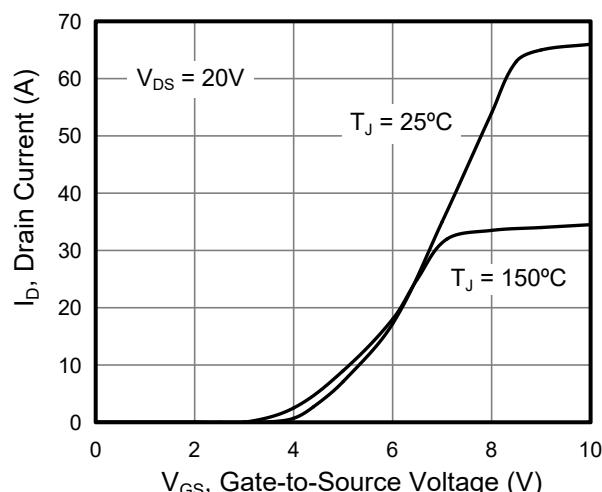


Figure 2. Transfer Characteristics

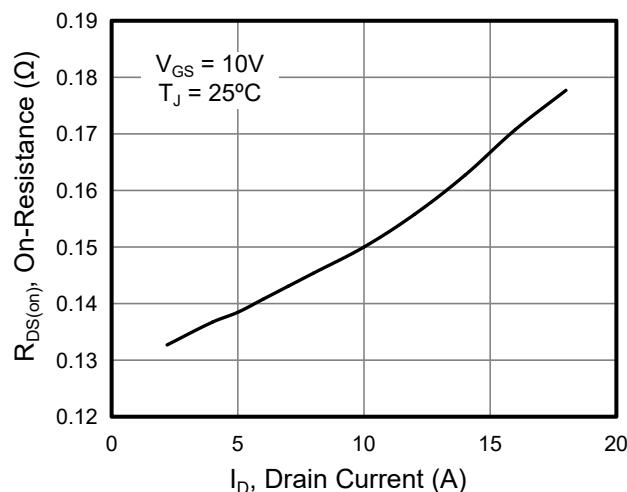


Figure 3. On-Resistance vs. Drain Current

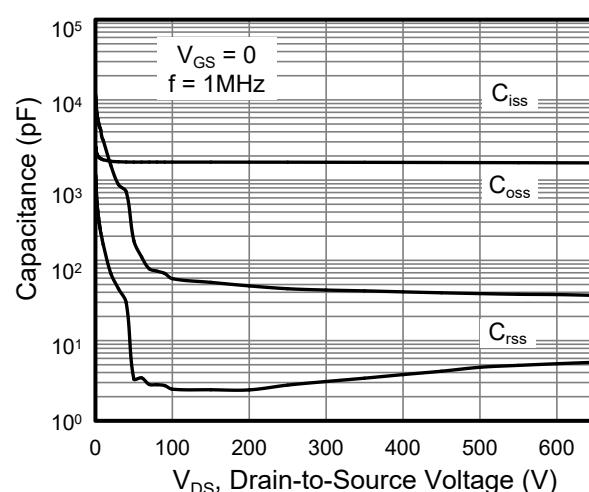


Figure 4. Capacitance

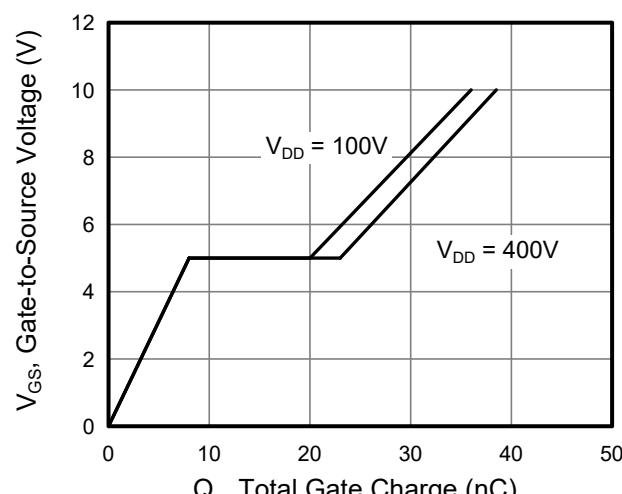


Figure 5. Gate Charge

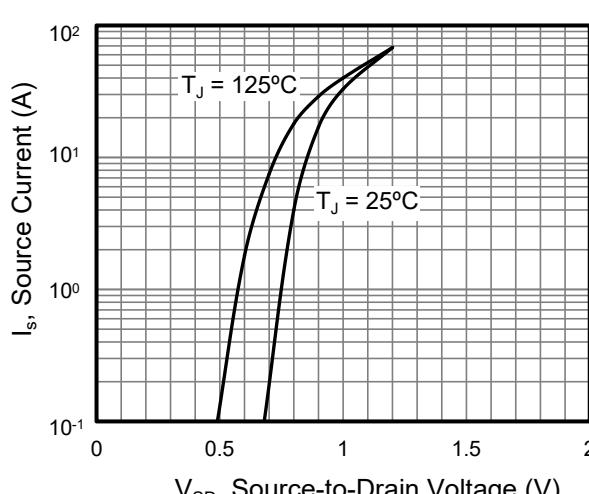
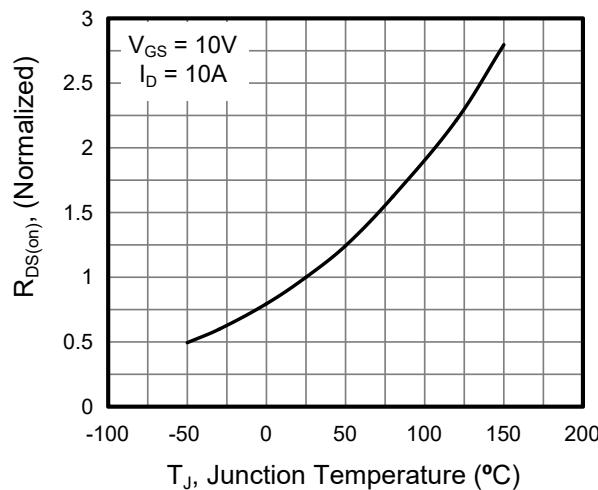
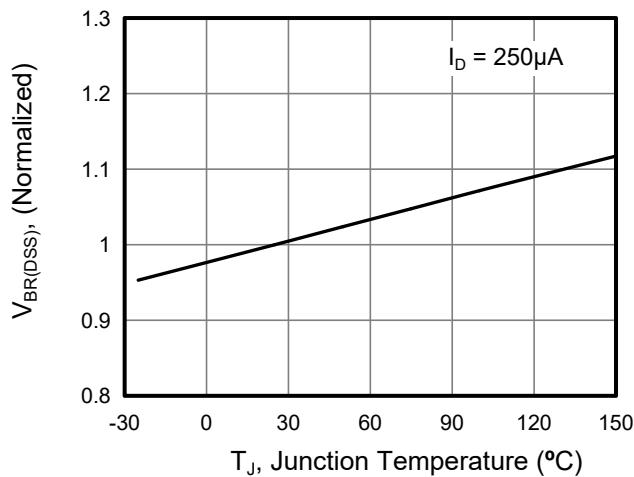


Figure 6. Body Diode Forward Voltage

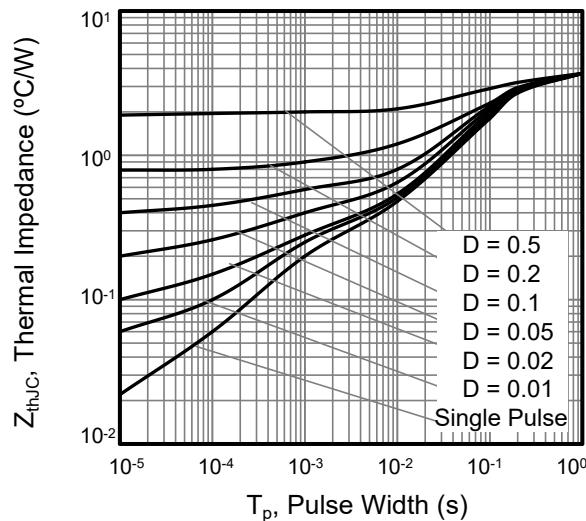
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**Figure 7. On-Resistance vs.
Junction Temperature**



**Figure 8. Breakdown voltage vs.
Junction Temperature**



**Figure 9. Transient Thermal Impedance
TO-220F**

600V Super Junction Power MOSFET

TO-220F Package Outline Data

