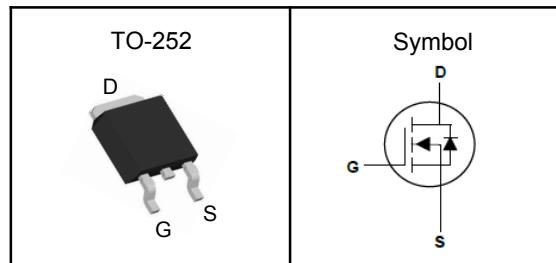


## N-Channel Enhancement Mode MOSFET

### Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

### Pin Description



### Applications

- Power Management in Desktop Computer
- DC/DC Converters

$V_{DSS}$	650	V
$R_{DS(ON)-Typ}$	2.1	$\Omega$
$I_D$	4	A

### Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
$V_{DSS}$	Drain-Source Voltage	650	V
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$E_{AS}$	Single Pulse Avalanche Energy <sup>③</sup>	240	mJ
$I_{DM}^{①}$	Pulse Drain Current Tested	16	A
$I_D$	Continuous Drain Current	$T_c=25^\circ\text{C}$	A
$P_D$	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	W

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sub>1</sub> (Max)	60	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sub>1</sub>	1.66	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C.

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.

## N-Channel Enhancement Mode MOSFET

### Electrical Characteristics ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	650	---	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=650\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_{\text{D}}=250\mu\text{A}$	2	---	4	V
$I_{\text{GSS}}$	Gate Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=2\text{A}$	---	2.1	2.4	$\Omega$
<b>Dynamic Characteristics<sup>⑤</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=25\text{V}$ , Freq.=1MHz	---	490	---	pF
$C_{\text{oss}}$	Output Capacitance		---	45	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	6	---	
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=325\text{V}$ , $R_{\text{G}}=25\Omega$ , $I_{\text{D}}=4\text{A}$	---	20	---	nS
$T_r$	Turn-on Rise Time		---	55	---	
$T_{\text{d(off)}}$	Turn-off Delay Time		---	70	---	
$T_f$	Turn-off Fall Time		---	50	---	
$Q_g$	Total Gate Charge	$V_{\text{DD}}=400\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=4\text{A}$	---	10	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	2	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	5	---	
<b>Source-Drain Characteristics (<math>T_J=25^\circ\text{C}</math>)</b>						
$V_{\text{SD}}$	Diode Forward Voltage <sub>2</sub>	$V_{\text{GS}}=0\text{V}$ , $I_{\text{S}}=4\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.4	V
$t_{\text{rr}}$	Reverse Recovery Time	$V_R=400\text{V}$ , $I_F=4\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	390	---	nS
$Q_{\text{rr}}$	Reverse Recovery Charge		---	2.2	---	nC

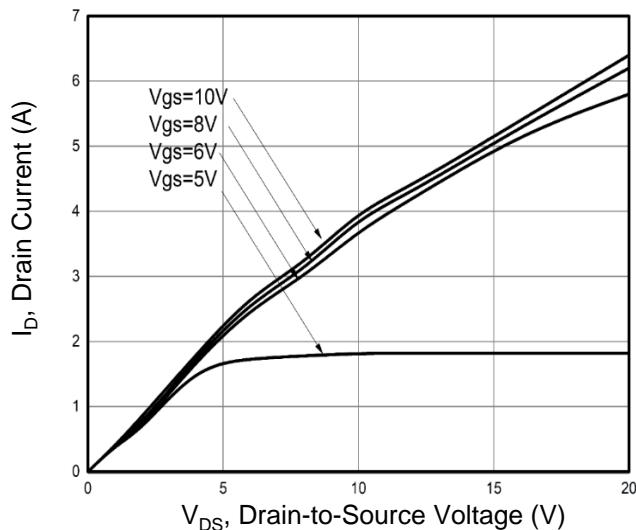
Note ④ : Pulse test (pulse width $\leq 300\mu\text{s}$ , duty cycle $\leq 2\%$ ).

Note ⑤ : Guaranteed by design, not subject to production testing.

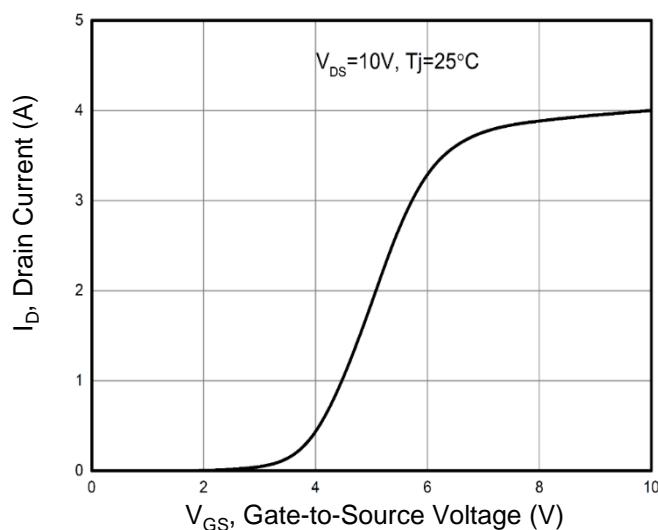
## N-Channel Enhancement Mode MOSFET

### Typical Characteristics

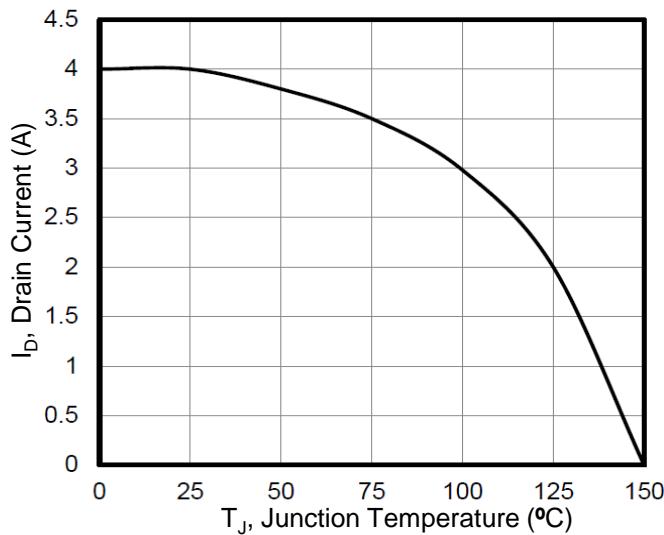
**Figure 1. Output Characteristics**



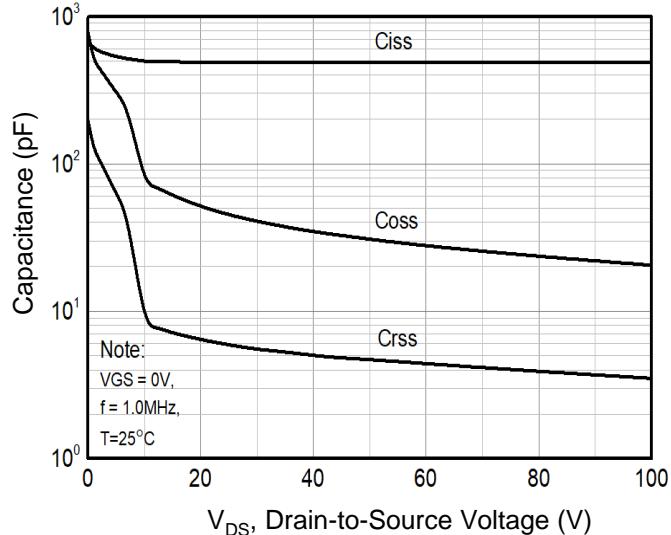
**Figure 2. Transfer Characteristics**



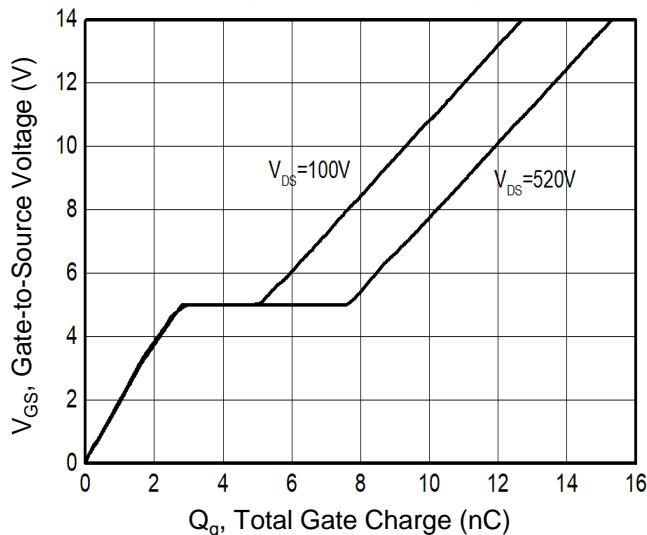
**Figure 3. Drain Current vs. Temperature**



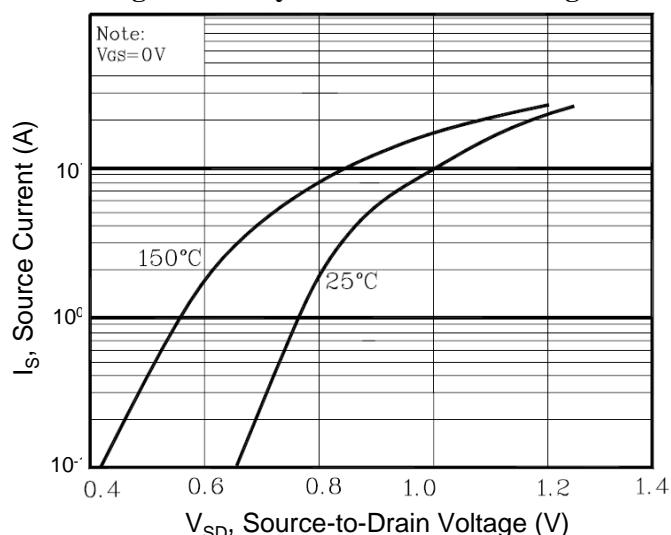
**Figure 4. Capacitance**



**Figure 5. Gate Charge**

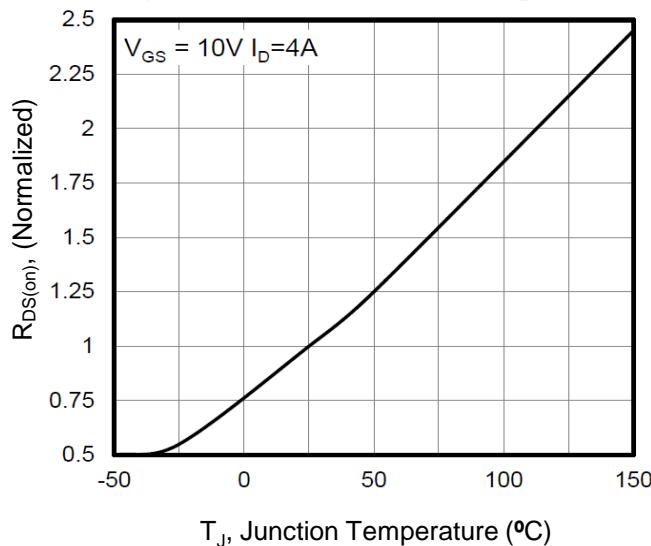


**Figure 6. Body Diode Forward Voltage**

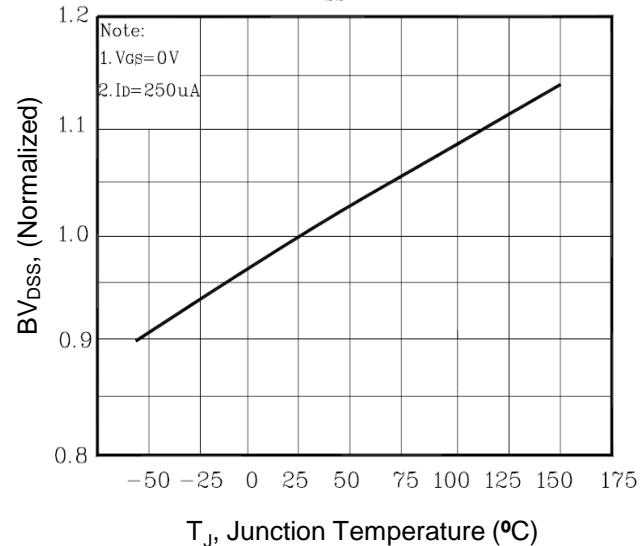


## N-Channel Enhancement Mode MOSFET

**Figure 7. On-Resistance vs. Temperature**

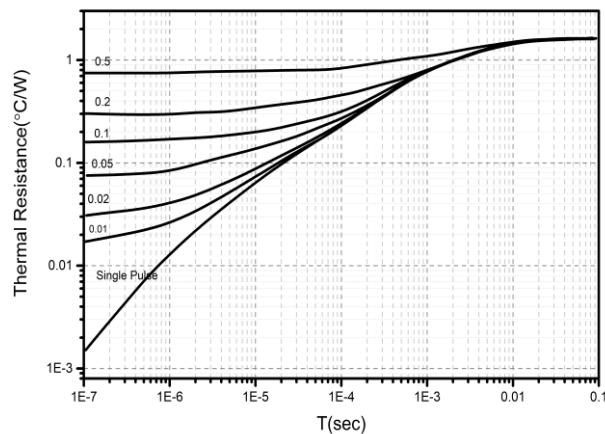


**Figure 8.  $BV_{DSS}$  vs. Temperature**



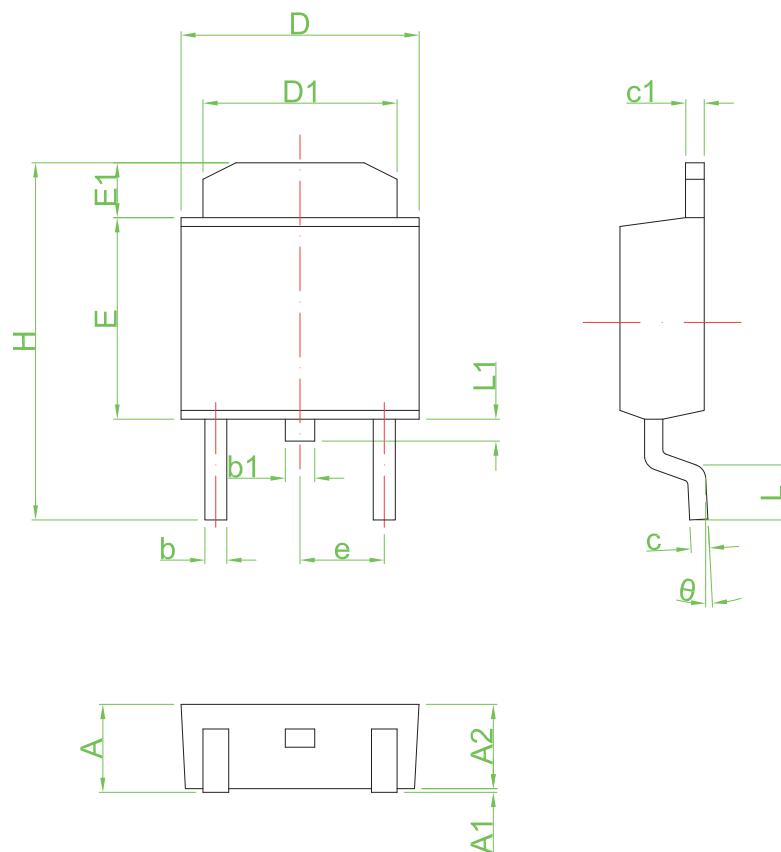
**Figure 9. Transient Thermal Impedance**

(TO-220 TO-252 )



## N-Channel Enhancement Mode MOSFET

### TO-252 Package Outline Dimensions



Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	2.25	2.65	0.089	0.104
A1	0.00	0.15	0.000	0.006
A2	2.20	2.40	0.087	0.094
b	0.50	0.70	0.020	0.028
b1	0.70	0.90	0.028	0.035
c	0.46	0.66	0.018	0.026
c1	0.46	0.66	0.018	0.026
D	6.30	6.70	0.248	0.264
D1	5.20	5.40	0.205	0.213
E	5.30	5.70	0.209	0.224
E1	1.40	1.60	0.055	0.063
H	9.40	9.90	0.370	0.390
e	2.30 TYP		0.09 TYP	
L	1.40	1.77	0.055	0.070
L1	0.50	0.70	0.020	0.028
θ	0°	8°	0°	8°