

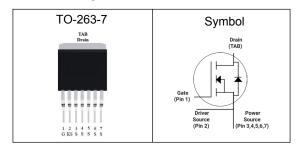
Features

- High blocking voltage with low on-resistance
- •High-speed switching with low capacitances
- •Fast intrinsic diode with low reverse recovery (Qrr)
- ●Easy to parallel
- ●RoHS compliant

Applications

- •Switch Mode Power Supplies
- DC/DC converters
- Solar Inverters
- Battery Chargers
- Motor Drives

Pin Description



V _{DS}	1200	V		
R _{DS(ON)-Typ}	80	mΩ		
I _D	42	А		

Absolute Maximum Ratings(T_C=25 °C, Unless Otherwise Noted)

Symbol	Parameter	Value	Unit
V _{DS}	Drain-Source Voltage	1200	V
I _D	Continuous Drain Current	42	Α
I _{D, pulse}	Pulse Drain Current Tested	80	Α
V _{GSmax}	Maximum Gate Source Voltage	-10/+25	V
$V_{GS,op}$	Recommend Gate Source Voltage	-5/+20	V
P _D	Maximum Power Dissipation	208	W
T _J	Maximum Junction Temperature	-55 to 175	°C
T _{STG}	Storage Temperature Range	-55 to 175	°C

Thermal Characteristics

Symbol	Parameter	Value	Unit
R _{eJC}	Thermal Resistance-Junction to Case	0.68	°C/W



Electrical Characteristics (TJ=25 $^{\circ}$ C, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit	
Static Electrical Characteristics							
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =100uA	1200			V	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =1200V, V _{GS} =0V		10	100	uA	
V _{GS(th)}	Gate Threshold Voltage	V_{DS} = V_{GS} , I_{D} = 5 mA	2	3	4	V	
Igss	Gate Leakage Current	V _{GS} =20V, V _{DS} =0V			250	uA	
R _{DS(ON)}	Drain-Source On-state Resistance	V _{GS} =20V, I _D =20A		80	100	mΩ	
	Dynamic Characteristics						
R _{G(int)}	Internal Gate Resistance	f=1MHz, V _{AC} =25 mV		4		Ω	
C _{iss}	Input Capacitance			1128		pF	
C _{oss}	Output Capacitance	V _{DS} =1000V, V _{GS} =0V,		86			
Crss	Reverse Transfer Capacitance	V _{GS} =UV, f=1MHz		5			
Eoss	Coss Stored Energy			44		μJ	
T _{d(on)}	Turn-on Delay Time			18		nS	
Tr	Turn-on Rise Time	V_{DS} =800V, V_{GS} =-5/+20V, I_{D} =20A, $R_{G(ext)}$ =2.5 Ω		65			
T _{d(off)}	Turn-off Delay Time			36			
T _f	Turn-off Fall Time			15			
Qg	Total Gate Charge	V _{DS} =800V,V _{GS} =-5/+20V, I _D =20A		52			
Q _{gs}	Gate-Source Charge			17		nC	
Q_{gd}	Gate-Drain Charge			15			
Source-Drain Characteristics							
Is	Continuous Diode Froward Current	V _{GS} = 0V		42		А	
V _{SD}	Diode Forward Voltage	I _S =10A, V _{GS} =0V		4		V	
t _{rr}	Reverse Recovery Time	$V_{DS} = 800 \text{V}, I_{S} = 20 \text{A}, \ V_{GS} = -5 \text{V} \ \text{dif/dt} = 2100 \text{ A/}\mu \text{s}$		26		nS	
Qrr	Reverse Recovery Charge			163		nC	
I _{rrm}	Peak reverse recovery current			12		Α	



Typical Performance Characteristics

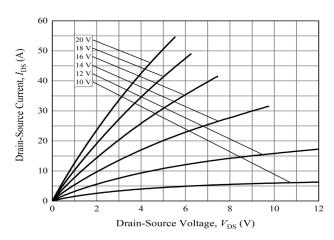


Figure 1: Typical Output Characteristics at T_J =-55 °C

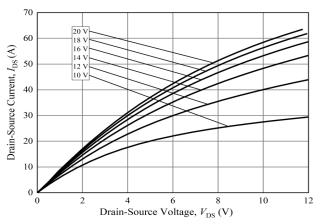


Figure 3: Typical Output Characteristics at $T_J=175$ °C

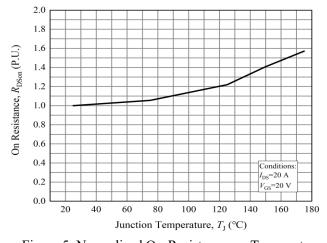


Figure 5: Normalized On-Resistance vs. Temperature

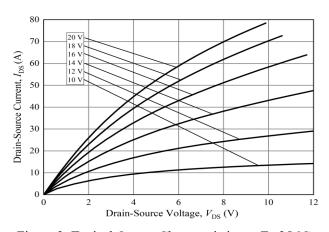


Figure 2: Typical Output Characteristics at T_J =25 °C

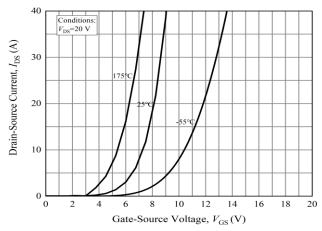


Figure 4: Typical Transfer Characteristics for Various Temperature

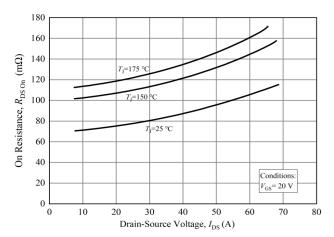


Figure 6: On-Resistance vs. Drain Current for Gate Various Temperatures

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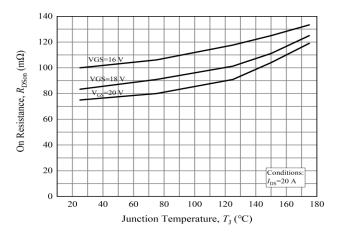


Figure 7: On-Resistance vs. Temperature for Various Voltage

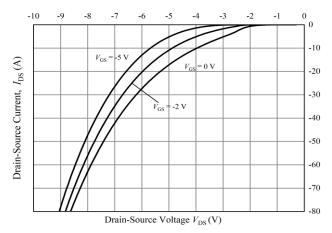


Figure 9: Typical Body Diode Characteristics at T_1 =25 °C

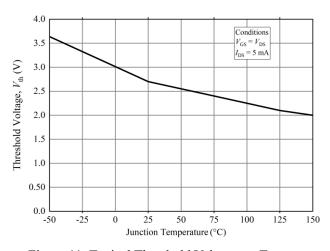


Figure 11: Typical Threshold Voltage vs. Temperature

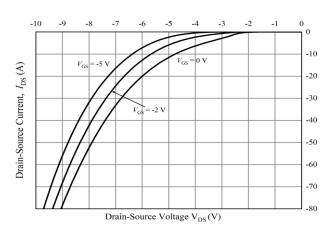


Figure 8: Typical Body Diode Characteristics at T_J =-55 °C

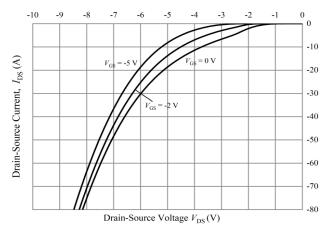


Figure 10: Typical Body Diode Characteristics at T_1 =175 °C

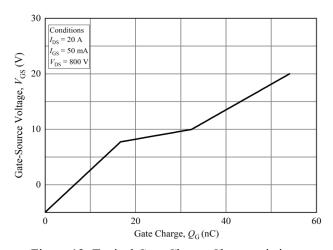


Figure 12: Typical Gate Charge Characteristics at T_1 =25 °C



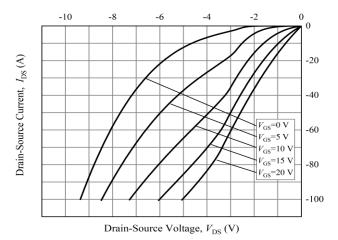


Figure 13: Typical 3rd Quadrant Characteristics T_J =-55 °C

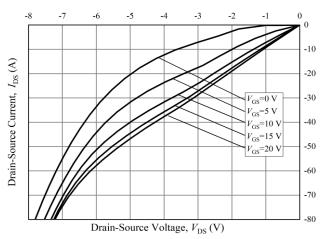


Figure 15: Typical 3rd Quadrant Characteristics at T_J =175 °C

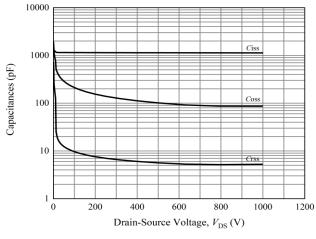


Figure 17: Typical Capacitances vs. Drain-Source Voltage

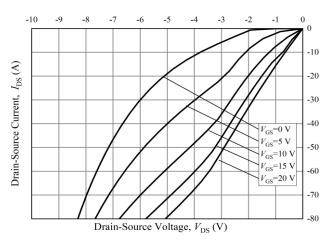


Figure 14: Typical 3rd Quadrant Characteristics at T_J =25 °C

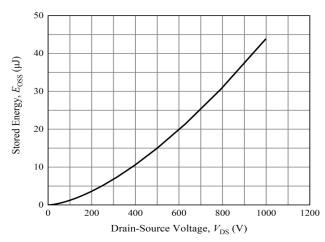


Figure 16: Typical Output Capacitor Stored Energy

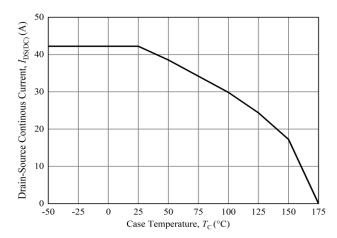


Figure 18: Continuous I_{DS} Current Derating Curve



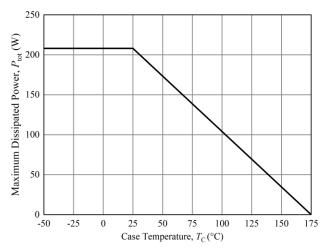
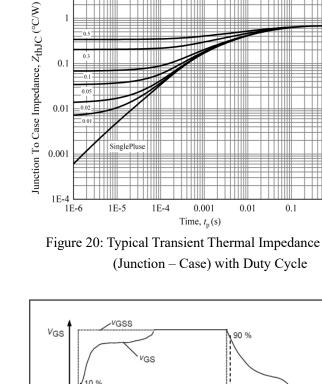


Figure 19: Power Dissipation Derating Curve



0.01

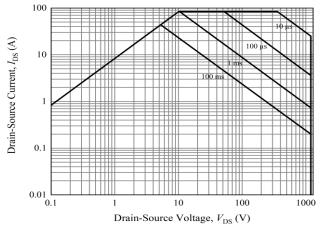
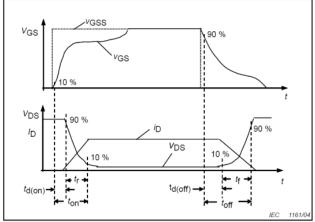


Figure 21: Safe Operate Area



0.001

Time, $t_{\rm p}(s)$

Figure 22: Resistive Switching Time Description



Package: TO-263-7

