

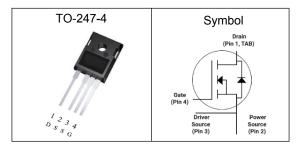
Features

- High blocking voltage with low on-resistance
- •High-speed switching with low capacitances
- •Fast intrinsic diode with low reverse recovery (Qrr)
- ●Easy to parallel
- ●RoHS compliant

Applications

- •Switch Mode Power Supplies
- DC/DC converters
- Solar Inverters
- Battery Chargers
- Motor Drives

Pin Description



V _{DS}	1200	V	
R _{DS(ON)-Typ}	40	mΩ	
I _D	75	А	

Absolute Maximum Ratings(T_C=25 °C, Unless Otherwise Noted)

Symbol	Parameter	Value	Unit
V _{DS}	Drain-Source Voltage	1200	V
I _D	Continuous Drain Current	75	Α
I _{D, pulse}	Pulse Drain Current Tested	150	Α
V _{GSmax}	Maximum Gate Source Voltage	-10/+25	V
$V_{GS,op}$	Recommend Gate Source Voltage	-5/+20	V
PD	Maximum Power Dissipation	330	W
TJ	Maximum Junction Temperature	-55 to 175	$^{\circ}$
T _{STG}	Storage Temperature Range	-55 to 175	℃

Thermal Characteristics

Symbol	Parameter	Value	Unit
R _{θJC}	Thermal Resistance-Junction to Case	0.39	°C/W



$\textbf{Electrical Characteristics} \; (T_J \text{=-} 25\, ^{\circ}\text{C} \,, \, \text{Unless Otherwise Noted})$

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit	
	Static Electrical Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =100uA	1200			V	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =1200V, V _{GS} =0V		10	100	uA	
V _{GS(th)}	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_{D}=10mA$	2	3	4	V	
Igss	Gate Leakage Current	V _{GS} =20V, V _{DS} =0V			250	uA	
R _{DS(ON)}	Drain-Source On-state Resistance	V _{GS} =20V, I _D =40A		40	55	mΩ	
	Dynam	ic Characteristics			•		
R _{G(int)}	Internal Gate Resistance	f=1MHz, V _{AC} =25 mV		2		Ω	
C _{iss}	Input Capacitance			2190		pF	
Coss	Output Capacitance	V _{DS} =1000V, V _{GS} =0V,		153			
Crss	Reverse Transfer Capacitance	V _{GS} =UV, f=1MHz		8			
Eoss	Coss Stored Energy			83		μJ	
T _{d(on)}	Turn-on Delay Time			18			
Tr	Turn-on Rise Time	V _{DS} =800V,V _{GS} =-5/+20V,		65		nS	
T _{d(off)}	Turn-off Delay Time	$I_D=40A, R_{G(ext)}=2.5\Omega$		36			
T _f	Turn-off Fall Time			15			
Qg	Total Gate Charge			99			
Q _{gs}	Gate-Source Charge	$V_{DS}=800V, V_{GS}=-5/+20V, I_{D}=40A$		32		nC	
Q_{gd}	Gate-Drain Charge	10 10/1		29			
Source-Drain Characteristics							
Is	Continuous Diode Froward Current	V _{GS} = 0V		75		Α	
V _{SD}	Diode Forward Voltage	I _S =20A, V _{GS} =0V		4		V	
t _{rr}	Reverse Recovery Time	V _{DS} =800V,I _S =20A,		28		nS	
Qrr	Reverse Recovery Charge	$V_{GS} = -5V$		232		nC	
I _{rrm}	Peak reverse recovery current	dif/dt = 2100 A/μs		13		Α	



Typical Performance Characteristics

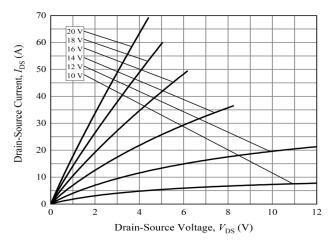


Figure 1: Typical Output Characteristics at T_J =-55 °C

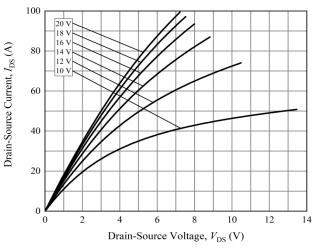


Figure 3: Typical Output Characteristics at $T_J=175$ °C

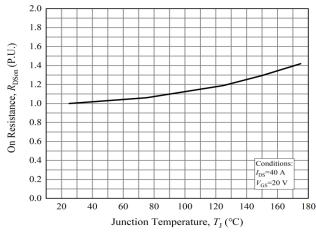


Figure 5: Normalized On-Resistance vs. Temperature

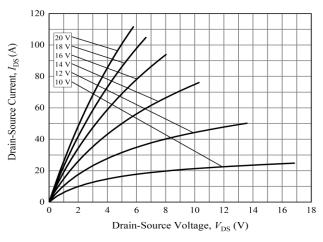


Figure 2: Typical Output Characteristics at T_J =25 °C

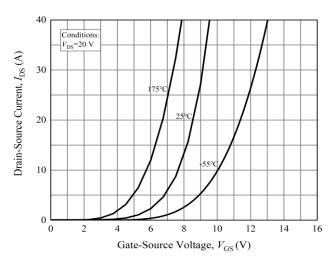


Figure 4: Typical Transfer Characteristics for Various Temperature

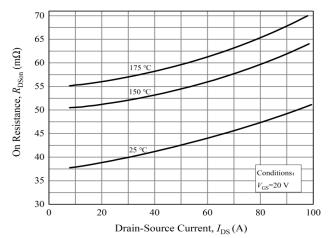


Figure 6: On-Resistance vs. Drain Current for Gate Various Temperatures

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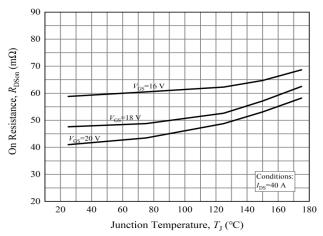


Figure 7: On-Resistance vs. Temperature for Various Voltage

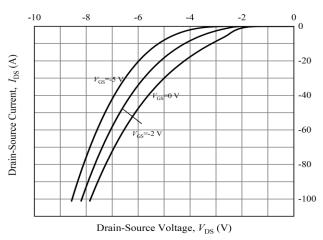


Figure 9: Typical Body Diode Characteristics at T_J =25 °C

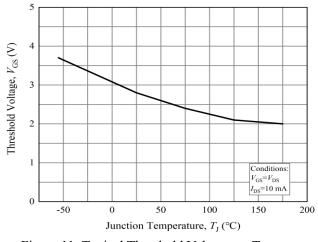


Figure 11: Typical Threshold Voltage vs. Temperature

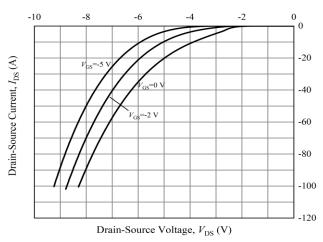


Figure 8: Typical Body Diode Characteristics at T_J =-55 °C

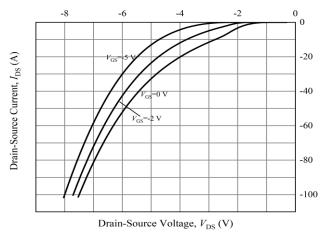


Figure 10: Typical Body Diode Characteristics at T_1 =175 °C

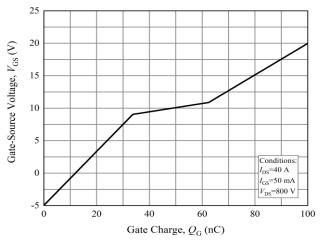


Figure 12: Typical Gate Charge Characteristics at T_J =25 °C

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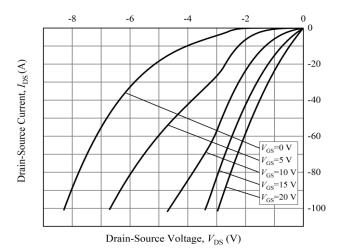


Figure 13: Typical 3rd Quadrant Characteristics T_1 =-55 °C

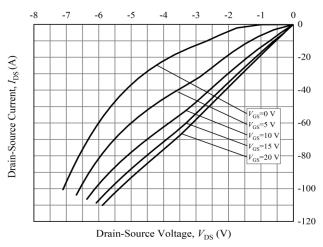


Figure 15: Typical 3rd Quadrant Characteristics at T_J =175 °C

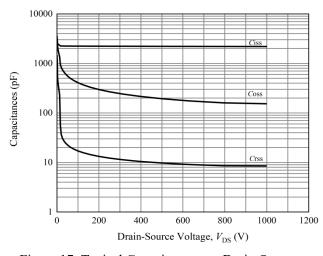


Figure 17: Typical Capacitances vs. Drain-Source Voltage

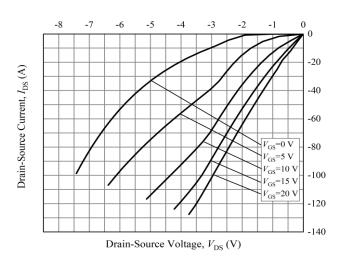


Figure 14: Typical 3rd Quadrant Characteristics at T_J =25 °C

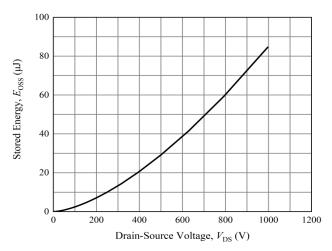


Figure 16: Typical Output Capacitor Stored Energy

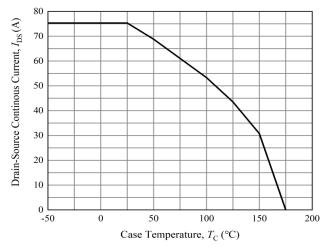


Figure 18: Continuous I_{DS} Current Derating Curve

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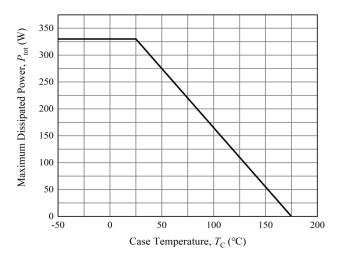


Figure 19: Power Dissipation Derating Curve

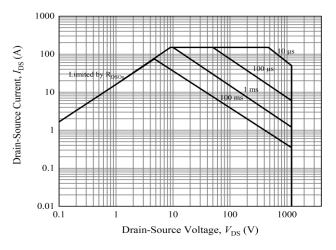


Figure 21: Safe Operate Area

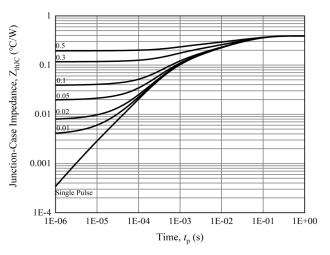


Figure 20: Typical Transient Thermal Impedance (Junction – Case) with Duty Cycle

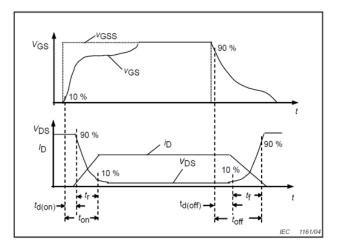
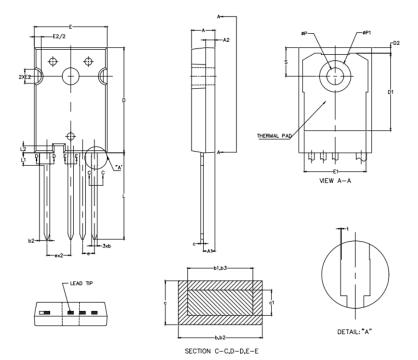


Figure 22: Resistive Switching Time Description



Package: TO-247-4



S					
M [mm		inch		
N>MBOLN	MIN.	MAX.	MIN.	MAX.	
Α	4.90	5.10	0.193	0.201	
A1	2.31	2.51	0.091	0.099	
A2	1.90	2.10	0.075	0.083	
Ь	1.16	1.26	0.046	0.050	
b1	1.15	1.22	0.045	0.048	
b2	2.16	2.26	0.085	0.089	
b3	2.15	2.22	0.085	0.087	
С	0.59	0.66	0.023	0.026	
с1	0.58	0.62	0.023	0.024	
D	22.40	22.60	0.882	0.890	
D1	16.25	16.85	0.640	0.663	
D2	1.05	1.35	0.041	0.053	
Е	15.75	15.90	0.620	0.626	
E1	13.26		0.552	_	
E2	2.90	3.10	0.114	0.122	
е	2.54	2.54BSC		0.1BSC	
L	18.30	18.60	0.720	0.732	
L1		2.80		0.110	
L2		1.50	_	0.059	
ØΡ	3.50	3.70	0.138	0.146	
øP1		7.40		0.291	
S	6.05	6.25	0.238	0.246	
t	0.00	0.15	0.000	0.006	